In the Specification:

Please amend the title on pages 1 and 30 as follows:

~~A Square-root Raised Cosine~~ Ultra-wideband Communications System Devices

Please amend paragraph [0045] as follows:

[0045] The timing generating unit 420 (Figure 4) can be used to generate signals to assist the receiver 500 in accurately tracking the received signal. By accurately tracking the received signal, the receiver 500 can maximize the quality of the received signal. The timing generating unit 420 can be implemented from a plurality of sample/hold units (such as sample/hold unit 535), whose function is to hold (sample) a signal at its input for a specified amount of time and to place an equivalent signal at its output. The sample/hold units can be used to provide samples of the two streams (in-phase and quadrature phase) with slightly different timings. For example, the sample/hold unit 535 can be used to provide on-time samples of the in-phase stream while another sample/hold unit 536 can be used to provide early and late samples of the in-phase stream. Similar sample/hold units 537 and 538 can provide similarly timed samples of the quadrature phase stream. The output of each of the sample/hold units can [[put]] be provided to an ADC (such as ADC 540), which converts the sample into its digital equivalent. The digital values provided by the ADCs may then be provided to a decoder and a despreader. According to a preferred embodiment of the present invention, there can be two different types of ADCs used for the ADCs. The ADCs coupled to the sample/hold units providing the on-time samples (such as sample/hold units 535 and 537), such as ADCs 540 and 542, can have higher resolution (3 bit ADCs, for example), while the ADCs coupled to the sample/hold units providing the early and late samples, such as ADCs 541 and 543, can have lower resolution (1 bit ADCs, for example). Both types of ADCs can operate at a similar conversion rate, such as 1.4 GHz. Note that since the original data that was transmitted was spread via a spreading code prior to transmission and that the early and late samples may only be used to help adjust time tracking, the use of a single bit ADC can be sufficient.